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U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date, if appropriate
JD	6,005,787	12-21-1999	Mizukoshi	363	71	—
JD	4,670,828	06-02-1987	Shekhawat et al.	363	56	—

FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Subclass	Translation
JD	WO 99/40676 A	08-12-1999	PCT	H02M	7) 797	yes
JD	JP 10 108474 A	04-24-1998	Japan	H02M	7) 48	abstract only

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

↑	Xiaoming Yuan et al.: "evaluation of soft switching techniques for the neutral-point-clamped (NPC) inverter.", IEEE, 1999, pages 659-663, XP010346776, figures 3,5,7,9
JD	"Medium Voltage Inverter using High-Voltage IGBTs" by A. Mertens, M. Bruckmann, R. Sommer, published in EPE'99 - Lausanne December
JD	"The Universal Medium Voltage Adjustable Speed Drive" by Y. Shakweh & E. A. Lewis, published in EPE'99 - Lausanne December
↓	A New Multilevel Inverter Topology with a Hybrid Approach", by Bum-Seok Suh, Yo-Han Lee, Dong-Seok Hyun and Thomas A. Lipo, published in EPE '99 - Lausanne December

Examiner: Sterrett Date considered: 5/6/04

*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.